

MONTE CARLO SIMULATION OF THE ELECTRON MOBILITY IN STRAINED SILICON

Siddhartha Dhar*, Enzo Ungersböck*, Mihail Nedjalkov, Vassil Palankovski

Advanced Materials and Device Analysis Group, at *

*Institute for Microelectronics, Technical University Vienna, Gusshausstrasse 27-29, 1040 Vienna, Austria, phone: +43 1 58801 36018, e-mail: dhar@iue.tuwien.ac.at

The SiGe/Si material system is recently a subject of increased research interest, since it provides beneficial band structure and transport properties due to strain. Monte Carlo method is used for analyzing these properties. Special focus is put on the description of the anisotropic majority/minority electron mobility in strained Si layers as a function of doping, electric field, and material composition.

Keywords: Monte Carlo simulation, electron mobility, strained Silicon

1. INTRODUCTION

The influence of strain on the intrinsic mobility of Si was first investigated in the early 1950s [1, 2]. The idea came to life again in the 1990s when it was first demonstrated that n-MOSFETs on strained substrates exhibit as much as 70% higher effective mobility than those on unstrained substrates [3]. Since then, the semiconductor industry has adopted several different technologies to introduce strain in the channels of MOS devices. For the CMOS technology, although the SiGe channel has been used to enhance the performance of PMOS transistors, it leads to a lack of improvement for the complementary NMOS transistors. The replacement of the channel material by strained Si, which utilizes an underlying relaxed SiGe [001] substrate for its functioning, renders a solution to the problem since it leads to enhancement of both the electron and hole mobilities. In order to investigate and design strained Si transistors, it is necessary to properly model the carrier mobilities in these devices.

2. SIMULATION RESULTS

Monte Carlo simulation which accounts for alloy scattering and the splitting of the anisotropic conduction band valleys due to strain in combination with an accurate ionized impurity scattering model, allowed us to obtain results for the low-field electron mobility in strained Si (SSi) for the complete range of donor and acceptor concentrations and Ge contents y in the $\text{Si}_{1-y}\text{Ge}_y$ buffer layer. The results obtained have been verified against experimental data which are available in the form of piezo-resistance coefficients. Fig. 1 (left) shows the in-plane (parallel) and the out-of-plane (perpendicular) minority electron mobility in strained Si as a function of the Ge content y in the $\text{Si}_{1-y}\text{Ge}_y$ buffer at 300 K for different acceptor doping concentrations.

The mobility obtained from piezo-resistance coefficients, which exhibits a linear increase for low strain levels [2, 4], is shown for comparison.

The difference between majority and minority electron mobilities [5] is a known phenomenon caused by effects such as degeneracy and the different screening behavior of electrons and holes in the semiconductor. An analytical model which describes this effect based on Monte Carlo simulation data is given in [6]. Fig. 1 (right) demonstrates a good match between the analytical model, our Monte Carlo simulation data, and measurements from [5, 7, 8, 9] for Si at 300 K. We use the same functional form to fit the doping dependence of the parallel and the perpendicular mobility in strained Si [10]. Fig. 1 (right) compares Monte Carlo simulation data with the model for the extreme case of strained Si on Ge.

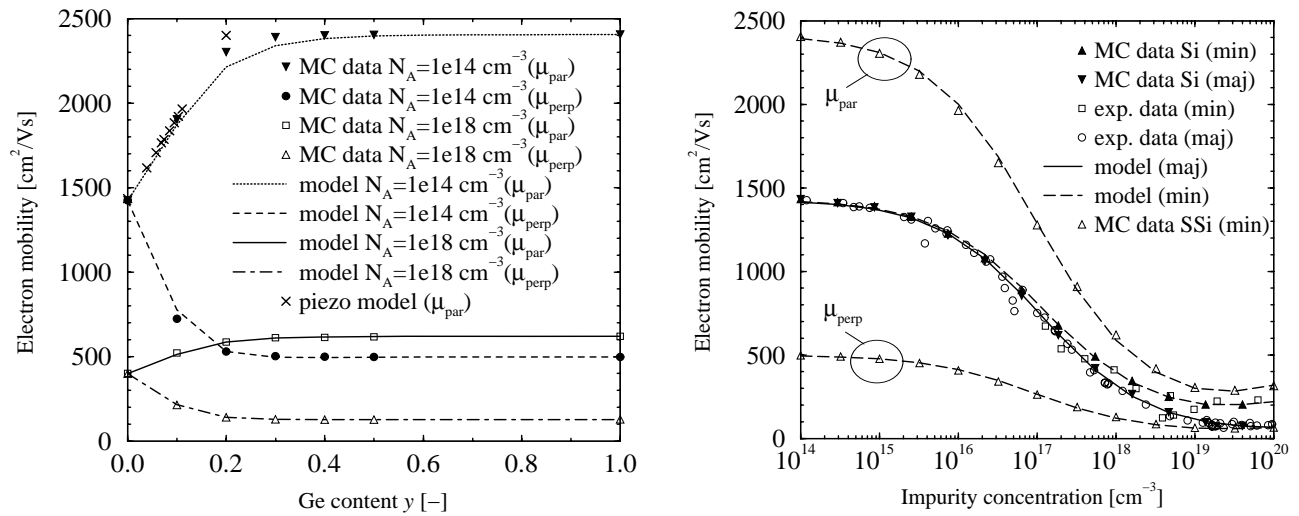


Figure 1. Parallel and perpendicular minority electron mobilities in strained Si as a function of the Ge content y in the $\text{Si}_{1-y}\text{Ge}_y$ [001] buffer layer for different acceptor doping concentrations (left). Comparison between Monte Carlo simulation data and experimental data for the majority and minority electron mobility in Si as a function of doping concentration; Parallel and perpendicular minority mobility in strained Si on Ge (right).

Fig. 2 shows the doping dependence of the in-plane minority and majority electron mobility components in strained Si layers for different Ge contents in the underlying SiGe for [001] orientation of the substrate. The solid lines depict the results as obtained from the analytical model, while the symbols indicate the Monte Carlo simulation results. As can be seen, the model reproduces the increase in minority electron mobility for high doping concentrations for all strain levels, when compared to majority electron mobility.

Fig. 3 (left) presents the velocity-field characteristics for unstrained and strained Si for [100] field direction as obtained from MC simulations. The simulation results agree well with measured data [11, 12] and other Monte Carlo simulation data [13, 14, 15]. Fig. 3 (right) shows the velocity-field characteristics as obtained from MC

simulations for biaxially strained Si grown on a relaxed SiGe substrate for different Ge contents and electric field along the in-plane [100] and out-of-plane [001] direction, respectively. The total velocity increases with strain for a field along the [100] direction and it decreases for a field along the [001] direction. For the in-plane electric field [100] the electron velocity exhibits a region of small negative differential mobility, typical for III-V semiconductor materials.

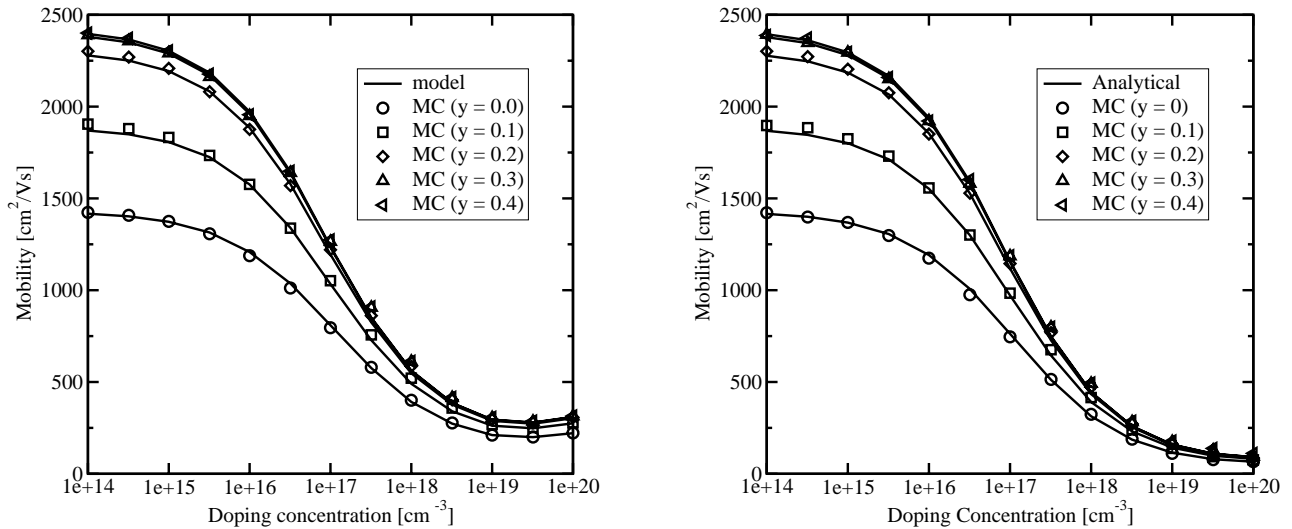


Figure 2. Doping dependence of the in-plane minority (left) and majority (right) electron mobility in strained Si calculated for different Ge contents in SiGe [001] substrate.

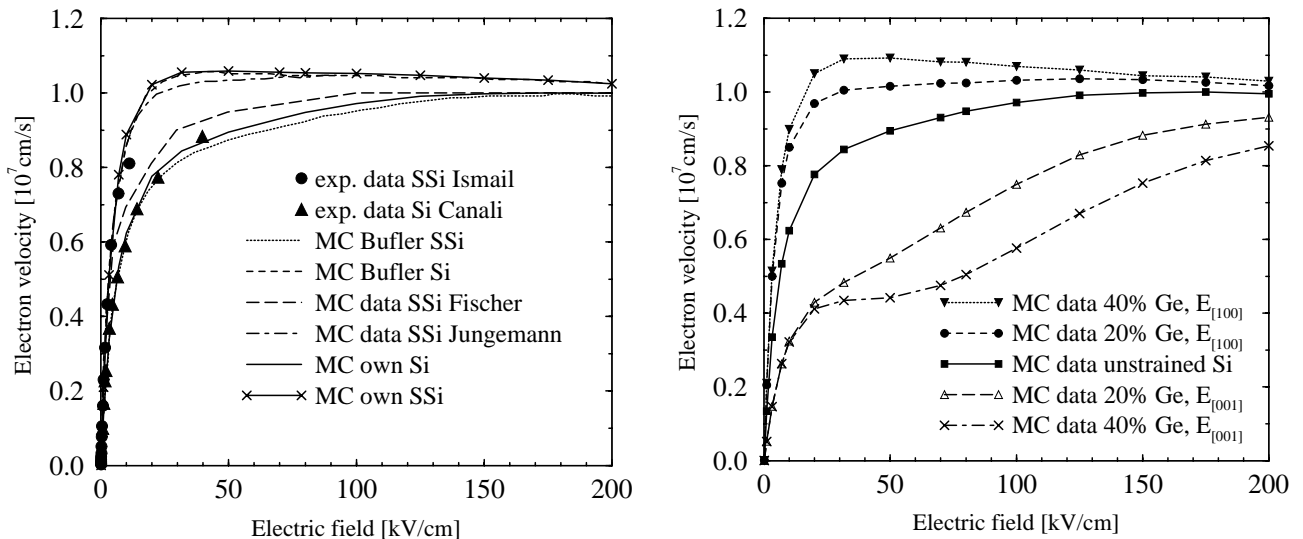


Figure 3. Electron velocity vs. electric field characteristics. Unstrained and strained Si on Si_{0.7}Ge_{0.3} for [100] field direction (left). Strained Si on SiGe with Ge content as a parameter for field along [100] and [001] directions (right).

3. CONCLUSION

We present results from Monte Carlo simulations of strained Si on SiGe [001] substrates. Electron mobilities are obtained for different conditions of doping, strain (substrate mole fractions), and electric field. The results are verified against measurements and other Monte Carlo simulation data. TCAD simulation tools need correct models of the strained Si/SiGe material system, especially with respect to carrier transport. Experimental data remain a basic input for verification of analytical TCAD models. However, Monte Carlo simulation data with confirmed accuracy can deliver information which is still experimentally missing.

ACKNOWLEDGMENT

Support from the Austrian Science Fund FWF, START Project No. Y247-N13 is acknowledged.

REFERENCES

- [1] H.H. Hall, J. Bardeen, and G.L. Pearson, "The Effects of Pressure and Temperature on the Resistance of p-n Junctions in Germanium", *Physical Review*, vol. 84, no. 1, 129-132, 1951.
- [2] C.S. Smith, "Piezoresistance Effect in Germanium and Silicon", *Physical Review*, vol. 94, no. 1, pp. 42-49, 1954.
- [3] J. Welser, J.L. Hoyt, and J.F. Gibbons, "NMOS and PMOS Transistors Fabricated in Strained Silicon/Relaxed Silicon-Germanium Structures", in *IEEE IEDM Tech. Dig.*, Dec. 1992, pp. 1000-1003.
- [4] Y. Kanda, "A Graphical Representation of the Piezoresistance Coefficients in Silicon", *IEEE Trans. Electron Dev.*, vol. 29, no. 1, pp. 64-70, 1982.
- [5] G. Masetti, M. Severi, and S. Solmi, "Modeling of Carrier Mobility Against Carrier Concentration in Arsenic-, Phosphorus- and Boron-Doped Silicon", *IEEE Trans. Electron Dev.*, vol. ED-30, no. 7, pp. 764-769, 1983.
- [6] V. Palankovski and R. Quay, *Analysis and Simulation of Heterostructure Devices*, Springer, Wien, New York, 2004.
- [7] K. Wolfstirn, "Hole and Electron Mobilities in Doped Silicon from Radiochemical and Conductivity Measurements", *J. Phys. Chem. Solids*, vol. 16, pp. 279-284, 1960.
- [8] S.E. Swirhun, D.E. Kane, and R.M. Swanson, "Measurements of Electron Lifetime, Electron Mobility and Band-Gap Narrowing in Heavily Doped p-type Silicon", *IEEE IEDM Tech. Dig.*, pp. 24-27, 1986.
- [9] I.Y. Leu and A. Neugroschel, "Minority-Carrier Transport Parameters in Heavily Doped p-type Silicon at 296 and 77 K", *IEEE Trans. Electron Dev.*, vol. 40, no. 10, pp. 1872-1875, 1993.
- [10] S. Dhar, H. Kosina, V. Palankovski, E. Ungersboeck, and S. Selberherr, "Electron Mobility Model for Strained-Si Devices", *IEEE Trans. Electron Dev.*, vol. 52, no. 4, pp. 527-533, 2005.
- [11] C. Canali, G. Ottaviani, and A. Alberigi-Quaranta, "Drift Velocity of Electrons and Holes and Associated Anisotropic Effects in Si", *J. Phys. Chem. Solids*, vol. 32, pp. 1707-1720, 1971.
- [12] K. Ismail, S. Nelson, J. Chu, and B. Meyerson, "Electron Transport Properties of Si/SiGe Heterostructures: Measurements and Device Applications", *Appl. Phys. Lett.*, vol. 63, pp. 660-662, 1993.
- [13] F. Bufler, P. Graf, S. Keith, and B. Meinerzhagen, "Full Band Monte Carlo Investigation of Electron Transport in Strained Si Grown on Si_{1-x}Ge_x Substrates", *Appl. Phys. Lett.*, vol. 70, pp. 2144-2146, 1997.

- [14] B. Fischer and K. Hofmann, "Full Band Monte Carlo Model for Electron and Hole Transport in Strained Si Including Inelastic Acoustic Phonon Scattering", Appl. Phys. Lett., vol. 74, pp. 2185-2187, 1999.
- [15] C. Jungemann and B. Meinerzhagen, Hierarchical Device Simulation - The Monte Carlo Perspective, Springer, Wien, New York, 2003.